

# 16-bit Microcontrollers

## KLCS-900/L Series

Low power consumption design ideal for high-performance portable equipment

### Features

- **Successive KLCS-900 architecture**
  - : Instruction compatible with KLCS-900 series
  - : General purpose 32-bit register
  - : Register bank format
- **Operative on a low power consumption**
  - : Clock gear function/Dual clock function
  - : Four standby modes
  - : Power consumption : 7mA(3V, 12.5MHz, typ.)
- **Operative on a low voltage 4.5V+0.55V @20MHz**
  - : Power supply voltage : 2.7V to 5.5V @12.5MHz
- **Minimum instruction execution time**
  - : 200nS (at 20MHz)
- **Linear address space**
  - : Program : 64k byte/16M bytes
  - : Data : 16M bytes

### Additional functions

- Built-in ROM
- 10-bit A/D converter
- Interrupt controllers
  - : 6 external interrupts, 14 internal interrupts
  - : 7-level priority can be set
- General purpose serial interface : 2channels
  - : Usable with a synchronous/an asynchronous transmission for both channels

### Application

- Digital movie cameras
- Portable MD players
- Cellular phones

## KLCS-900/L1 Series

Next-generation 16-bit microprocessors offering both high performance and low-power operation

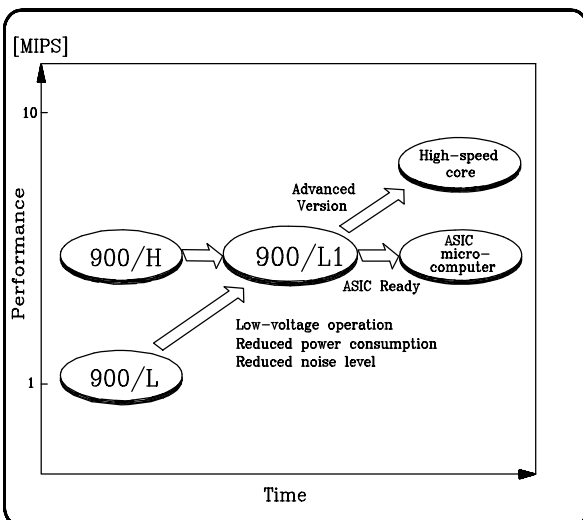
### Features

- **Low-voltage operation: 1.8V to 5.5V**
- **Low-power consumption:**
  - 3.0mA (when operating at 3V and 16MHz)
- **Low-noise (EMC register)**
  - EMI: reduced by 30%
  - EMS: noise filter, protection register

### Application

- DSCS
- DVCs
- Portable communication

### Core expansion



### Comparison of core performance(with 900/L)

