

# PLL MODULE FOR CELLULAR ANALOG AND DIGITAL SYSTEM

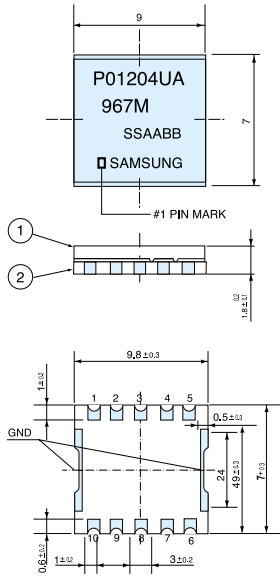


## FEATURES

- Surface Mounted Device Type
- $\mu$ -Strip Resonator(Quasi-TEM Mode,  $\lambda/4$ type)
- Low Current Consumption(12mA)
- Excellent C/N & S/N Ratio
- Low Operating Voltage(+3.0V)

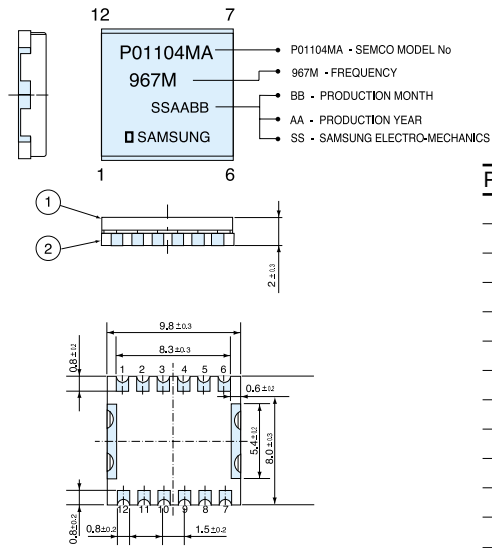
## APPLICATION GUIDE

PLL MODULE: Base Station, GPS, Mobile Telephone, PCN/PCS, Portable Telephone, Satellite Communication, TRS, Wireless Modem, WLL



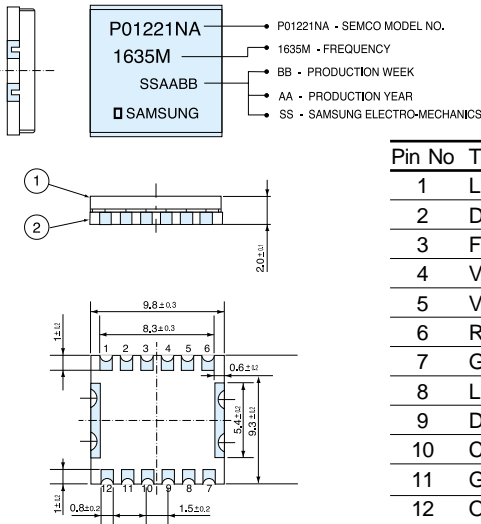
0.1cc PSM (CDMA)

Pin No	Terminal Name
1	RFOUT
2	VCC1(VCO)
3	VCC2(PLL)
4	Do LF
5	Fin IF
6	Xin
7	LD
8	CLK
9	DATA
10	LE



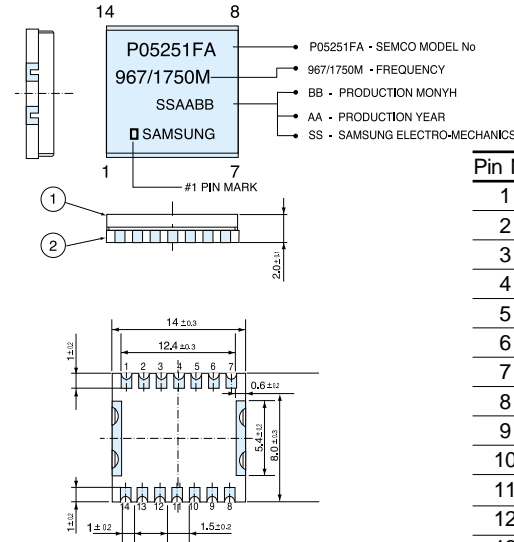
0.15cc PSM (CDMA)

Pin No	Terminal Name
1	LD
2	Do IF
3	fin IF
4	Vcc1(VCO)
5	Vcc2(PLL)
6	RF OUT
7	PS
8	LE
9	DATA
10	CLACK
11	GND
12	OSC in



0.18cc PSM (K-PCS)

Pin No	Terminal Name
1	LD
2	Do IF
3	Fin IF
4	Vcc1(VCO)
5	Vcc2(PLL)
6	RF OUT
7	GND1
8	LE
9	DATA
10	CLOCK
11	GND2
12	OSC in



0.26cc PSM (Dual Band: CDMA & US-PCS)

Pin No	Terminal Name
1	RF OUT1(High)
2	Vbb1
3	Do IF
4	Fin IF
5	Vcc2(PLL+VCO)
6	Vbb2
7	RF OUT2(LOW)
8	GND
9	LE
10	DATA
11	GND
12	OSC in
13	CLK
14	LD

## SPECIFICATIONS

APPLICATION	SIZE	MODEL	OPERATING Freq. (MHz)	OUTPUT POWER (dBm)	HARMONICS (dBc)	P/N (dBc/Hz) @100kHz offset	LOCK UP TIME (msec Max)	DIMENSION(mm)		
								LENGTH	WIDTH	HEIGHT
CDMA	0.1cc	PO1204UA	954~980	-6~0	≤-10	-119(Typ)	15	9.0±0.3	7.0±0.3	1.8±0.2
	0.15cc	PO1104MA		-5~1	≤-13	-120(Typ)	20	9.8±0.3	8.0±0.3	2.0±0.2
	0.18cc	PO1204NC		-5~1	≤-13	-120(Typ)	20	9.8±0.3	9.3±0.3	2.0±0.2
K-PCS	0.1cc	PO1221UA	1620~1650	-3~3	≤-15	-114(Typ)	20	9.0±0.3	7.0±0.3	1.8±0.2
	0.15cc	PO1121MA		-3~3	≤-20	-116(Typ)	20	9.8±0.3	8.0±0.3	2.0±0.2
	0.18cc	PO1121NA		-3~3	≤-15	-116(Typ)	20	9.8±0.3	9.3±0.3	2.0±0.2
	0.18cc	PO1221NA		-3~3	≤-20	-116(Typ)	15	9.8±0.3	9.3±0.3	2.0±0.2
US-PCS	0.1cc	PO1241UA	1720~1780	-3~3	≤-15	-114(Typ)	20	9.8±0.3	9.3±0.3	1.8±0.2
	0.18cc	PO1241NA		-3~3	≤-20	-115(Typ)	20	9.8±0.3	9.3±0.3	2.0±0.2
DBTM	0.26cc	PO5252FA	1720~1780 954~980	-5~-2	≤-13 ≤-13	-115(Typ) -119(Typ)	20	14.0±0.3	9.3±0.3	2.0±0.2